

Ciprian-Romeo COMȘA, MSEE

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Personal Stuff

Date and place of birth – February 23rd 1977, Iași, România

Marital status – unmarried (yet)

Military status – recruit

Education

“Gheorghe Asachi” Technical University of Iași:

PhD student (extramural) under the direction of Prof. Dr. Eng. Ion Bogdan:

2000 – 2007

- Field – Electronics and Communications Engineering
- Thesis name – “Contributions in Modeling, Simulating and Performances Evaluation of Communications Systems”
- Stage – Thesis elaboration to be started

Master of science in Digital Radiocommunications:

2000 – 2001

- Total average: 9.85 (2nd out of 15)
- Dissertation paper: “Bluetooth Applications Introductory”

Communications Section of Electronics and Telecommunications Faculty:

1995 – 2000

- Graduated with average 9.51 (5th out of 54)
- Diploma exam passed with total average 9.62

Specializations and Qualifications

“Organizational Culture and European Integration” postgraduate course of CETEX Department of “Gheorghe Asachi” Technical University of Iași

2003

Courses of Didactical Training Department of “Gheorghe Asachi” Technical University of Iași, graduated with total average 9.25

1995 – 2000

Socrates Erasmus scholarship of 3 months, at The National Technical University of Athens, Greece, concluded with C++ & IDL implementation of a B-ISDN (ATM based network) signaling (call establishment messages) Parser based on CORBA, used as diploma thesis

2000

Certificate in CadStar CAD from Zuken-Redac-Design-System GmbH

1999

Degrees

Teaching Instructor at The Faculty of Electronics and Telecommunications of Iași

2004 – now

Teaching Assistant at The Faculty of Electronics and Telecommunications of Iași

2002 – 2004

Master of Science in Digital Radiocommunications

2001

Batchelor of Science in Electrical Engineering, Telecommunications specialization

2000

Working Experience

Working as communications design engineer, at S.C. “Isratech” S.R.L. Iași

2000 – 2002

1. Integrated circuits computer aided design, using Matlab with Simulink, HDL and C++ tools
2. Study of Bluetooth technology

Collaborations with “Gh. Asachi” Technical University, Iași, Electronics and Telecommunications Faculty, teaching:

2000 – 2002

1. Computer Programming and Programming Languages (C, C++)
2. Electronic Computer Aided Design (Spice and VHDL)

Teaching Assistant and Instructor at “Gheorghe Asachi” Technical University, Iași, Electronics and Telecommunications Faculty, teaching:

2002 - now

1. Object Oriented Programming (C++)
 2. Mobile Communications
 3. Digital Communications
1. Computer Programming and Programming Languages (C, C++)
 2. Electronic Computer Aided Design (Spice)

Interest Fields

Object Oriented Programming Techniques. Numerical Methods and Algorithms.

System Level Modeling and Simulation of Circuits for Telecommunications.

HDL Modeling of Circuits for Telecommunications.

Digital Communications Techniques. Bluetooth Technology. OFDM Technique. MIMO Systems.

Scientific Manifestations Participation

International Symposium – Ericsson Mobility World – Developers Day in Romania, J. B. Marriott Grand Hotel, September 29, Bucharest, Romania, 2004.

Member of Organization Committees

Member of the Local Organization Committee of The International Symposium on Signals, Circuits and Systems SCS2003, July 10-11, Iasi, Romania, 2003.

Foreign Languages

English - speak, read and write - medium

French - speak and read - medium

Research Project Participations

1.	Project Coordination: <i>National Institute of Studies and Researches in Communications Bucharest</i> Local project manager: <i>Prof. Dr. Eng. Ion Bogdan</i>			
	Title: <i>Methods and Solutions for Introduction of 3rd Generation Mobile Services in Romania</i>			
	Financing: <i>INFOSOC-C1</i>	Sum: <i>90 mil. lei / 2003</i>	Year: <i>2001-2003</i>	Stage: <i>Finished</i>
2.	Project Manager: <i>Instr. Eng. Lucian Stoica</i> Project respondent: <i>Instr. Eng. Ciprian Comşa</i>			
	Title: <i>VLSI Design of High Speed Prediction Circuits</i>			Contract No. 33557/2003, Theme 63, CNCSIS Code 358
	Financing: <i>CNCSIS-At</i>	Sum: <i>36 mil. lei + 85 mil. lei</i>	Year: <i>2003-2004</i>	Stage: <i>In progress</i>
3.	Project Manager: <i>Lect. Dr. Eng. Dănuţ Burdia</i>			
	Title: <i>Submicrometer CMOS Gates Driving Transmission Lines Modeling for Discrete Events Simulation and High Speed VLSI circuits performance evaluation</i>			Contract No. 33557/2003, Theme 54, CNCSIS Code 323
	Financing: <i>CNCSIS-At</i>	Sum: <i>36 mil. lei + 85 mil. lei</i>	Year: <i>2003-2004</i>	Stage: <i>In progress</i>
4.	Project Manager: <i>Instr. Eng. Ciprian-Romeo Comşa</i>			
	Title: <i>Modeling, Implementation and Performance Evaluation of an OFDM Receiver in High Speed Communication Systems.</i>			CNCSIS Code 171
	Financing: <i>CNCSIS-Td</i>	Sum: <i>50 mil. lei / 2004</i>	Year: <i>2004-2006</i>	Stage: <i>In progress</i>
5.	Project Coordination: <i>Prof. Dr. Eng. Ion Bogdan</i>			
	Title: <i>Simulink – Stateflow Modeling and Simulation of an WLAN – GSM System in Order to Implement a Dual Transceiver.</i>			
	Financing: <i>ASICAhead Bucharest</i>		Year: <i>2004</i>	Stage: <i>In progress</i>
6.	Project Coordination: <i>National Institute of Studies and Researches in Communications Bucharest</i> Local project manager: <i>Prof. Dr. Eng. Ion Bogdan</i>			
	Title: <i>Bluetooth Access to 4G System applied in personal home networks implementation.</i>			
	Financing: <i>INFOSOC-C6</i>	Sum: <i>120 mil. lei / 2004</i>	Year: <i>2004-2006</i>	Stage: <i>In progress</i>

Papers

Articles Printed (or Accepted for Printing) in National Scientific Publications

- [1] **Comşa C. R.**, Bogdan I., “*Bluetooth, present and perspectives*”, “Telecomunicații” revue, No. 2/2001, Bucharest, pp. 62-86.
- [2] **Comşa C. R.**, Bogdan I., “*OFDM, Coded Modulation Technique*”, “Telecomunicații” revue, No. 2 / 2003, Bucharest, pp. 30-38.
- [3] **Comşa C. R.**, Grigore G., “*FIR Filters Implementation Approaches*”, Scientific Bulletin of “Gh. Asachi” Technical University of Iasi, Section of Electrotechnics, Energetics and Electronics, tom XLIX (LIII), fasc. 3-4/2004.

Articles Printed (or Accepted for Printing) in Proceedings of International Conferences from Romania

- [4] **Comşa C. R.**, Alecsandrescu I., Bogdan I., Maiorescu A., „*Simulation Model for Mobile Radio Channels*”, European Conference on Intelligent Technologies ECIT2002, Iași, România, July 17-20, 2002, CD-ROM volume.
- [5] **Comşa C. R.**, Bogdan I., „*System Level Design of Baseband OFDM for Wireless LAN*”, Proceedings of the International Symposium on Signals, Circuits and Systems SCS2003, July 10-11, Iasi, Romania, 2003, pp. 313-316.
- [6] Bogdan I., **Comşa C. R.**, “*Analysis of Circular Arrays as Smart Antennas for Cellular Networks*”, Proceedings of the International Symposium on Signals, Circuits and Systems SCS2003, July 10-11, Iasi, Romania, 2003, pp. 525-528.
- [7] **Comşa C.**, Beldianu F., Cotae P., “*Windowing Techniques for OFDM Systems*”, Proceedings of the Symposium of Electronics and Telecommunications ETc2004, Sixth Edition, "Politehnica" University of Timisoara, Faculty of Electronics and Telecommunications & IEEE Romania Section, Timisoara, Romania, October 22-23, 2004.
- [8] **Comşa C.**, Burdia D., Chiper D., “*Implementation of an OFDM Synchronizer*”, Proceedings of the Symposium of Electronics and Telecommunications ETc2004, Sixth Edition, "Politehnica" University of Timisoara, Faculty of Electronics and Telecommunications & IEEE Romania Section, Timisoara, Romania, October 22-23, 2004.
- [9] Chiper D. F., **Comşa C.**, “*An efficient linear systolic array architecture for a memory-based VLSI implementation of Type III generalized Hartley transform*”, Proceedings of the Symposium of Electronics and Telecommunications ETc2004, Sixth Edition, "Politehnica" University of Timisoara, Faculty of Electronics and Telecommunications & IEEE Romania Section, Timisoara, Romania, October 22-23, 2004.
- [10] Burdia D., **Comşa C. R.**, Ionaşcu C., “*Short-circuit power evaluation of deep submicrometer CMOS gates driving lossless transmission lines*”, Proceedings of the Symposium of Electronics and Telecommunications ETc2004, Sixth Edition, "Politehnica" University of Timisoara, Faculty of Electronics and Telecommunications & IEEE Romania Section, Timisoara, Romania, October 22-23, 2004.

Articles Printed (or Accepted for Printing) in Proceedings of International Conferences

[11] Burdia D., Bozomitu R. G., Comşa C. R., „Some Aspect son Modeling and Characterization of Deep Submicrometer CMOS Gates Driving Lossless Transmission Lines”, Proceedings of the 27th International Spring Seminar on Electronics Technology ISSE2004, Sofia, Bulgaria, May 13-16, 2004.

Articles Printed (or Accepted for Printing) in Other Publications

[12] Comşa C. R., “Bluetooth Technology”, “Comunicații Mobile” revue, February 2002, Bucharest, pp. 38-39.

Projects as student

Digital Integrated Circuits	1997
✓ The design of an automaton with DIC's for combinational CMOS digital integrated circuits (DIC's) testing	
Electronics Devices and Circuits	1997
✓ The component level design of a stabilized voltage supply with bipolar transistors. The project was validated by an Electronics-Workbench EDA simulation	
✓ The component level design of a low frequency, power amplifier with bipolar transistors. A Spice simulation validated the project	
Analog Integrated Circuits	1998
✓ The design at component level of a stabilized voltage supply with operational amplifier	
Computer Aided Design	1999
✓ The project included:	
✓ Editing and functional analysis of a counter circuit, using EED-3, Electronics Workbench and Spice	
✓ Design of the logic devices of circuit, using 0.8micro CMOS technology parameters	
✓ Developing analog parametric models, emphasizing the delays by temperature, using Spice simulations	
✓ Developing digital structural and behavioral models, with two and nine logic levels and realistic delays, using VHDL	
Interconnection Techniques in Electronics	1999
✓ Schematic and package (SCM & PCB) design of various circuits, using CadStar	
✓ Zuken-Redac-Design-System GmbH, München, Germany, reworted this project with a CadStar Training Certificate	
Electronic Technology	2000
✓ The design of an AC adapter	
Radioemission Bases	2000
✓ The design of a radio-frequency, power amplifier with electronic tubes and a radio-frequency, power amplifier with transistors	
Software for Telecommunications	2000
✓ The design of a personal web page, using HTML (HyperText Markup Language)	
Diploma Thesis	2000
✓ The design and C++ implementation of a parser, which intercepts and interprets B-ISDN (Broadband Integrated Services Digital Network)(ATM based network) signaling (call establishment messages) and invokes IDL-CORBA (Interface Definition Language – Common Object Request Broker Architecture) methods	
✓ The aim is to modify a B-ISDN in order to achieve an Intelligent Network	
✓ This project was realized by the support of team from Telecommunications Laboratory, National Technical University of Athens, Greece	
Advanced Systems of Communication	2001
✓ Power Spectral Density of MLT 3-5 line code, using Maple and Mathematica tools and C++ language	
Dissertation Paper	2001
✓ Introduction in Bluetooth applications	

Courses taken (during the 6 years of university degree)

First year

Exams

- ✓ Computer Programming
- ✓ Electrical Engineering Fundamentals
- ✓ Elements of Mechanical Engineering
- ✓ Linear Algebra, Analytic Geometry and Differential
- ✓ Equations
- ✓ Mathematical Analysis
- ✓ Passive Components and Circuits
- ✓ Physics

Examinations

- ✓ Descriptive Geometry and Mechanical Drawing
- ✓ English Language
- ✓ Philosophy
- ✓ Physical Training
- ✓ Productive Activity
- ✓ Scholar Psychology

Second year

Exams

Examinations

- ✓ Computer Aided Design
- ✓ Digital Integrated Circuits
- ✓ Electronic Materials
- ✓ Electronics Devices and Circuits
- ✓ Electrical Engineering Fundamentals
- ✓ Special Mathematics
- ✓ Physics
- ✓ Signals, Circuits and Systems

- ✓ English Language
- ✓ Pedagogy
- ✓ Productive Activity
- ✓ Physical Training

Third year

Exams

- ✓ Analog Integrated Circuits
- ✓ Architecture of Computing Systems and Operation Systems
- ✓ Microprocessor Systems Architecture
- ✓ Electronic Measurements
- ✓ Electronics Devices and Circuits
- ✓ Introduction in Communications
- ✓ Signals, Circuits and Systems
- ✓ Television
- ✓ Theory of Information Transmission

Examinations

- ✓ English Language
- ✓ Productive Activity
- ✓ Probability Theory

Fourth year

Exams

- ✓ Computer Aided Design
- ✓ Digital Communications
- ✓ Digital Signal Processing
- ✓ Electro-acoustics
- ✓ Electromagnetic Compatibility
- ✓ Electronic Devices for Measure and Control
- ✓ Electronic Technology
- ✓ Microwave Technique
- ✓ Object Oriented Programming
- ✓ Radioemission Bases
- ✓ Systems of Communications

Examinations

- ✓ Economy
- ✓ English Language
- ✓ Electronic Viability

Fifth year

Exams

- ✓ Electronic Telephone Exchanges
- ✓ Digital Communications Systems
- ✓ Lines and Systems of Telephone Transmissions
- ✓ Mobile Communications
- ✓ Optical Communications
- ✓ Software for Telecommunications
- ✓ Spatial Communications

Examinations

- ✓ Adaptive Filters
- ✓ ISDN
- ✓ Marketing

Sixth year (Master)

Exams

- ✓ Advanced Systems of Digital Communications
- ✓ Theory of Estimation and Adaptive Filtering
- ✓ Electromagnetic Compatibility in Communication Systems
- ✓ Advanced Systems of Communications 2
- ✓ Mobile Communication Systems
- ✓ Computer Aided Design of Communication Systems