

**International Symposium for Design and Technology of Electronic Packages
SIITME 2006 IASI ROMANIA**

SYMPOSIUM PROGRAM

Thursday, September 21, 2006

- 12:00 – 18:00 **Registration** – The “Lost Steps Hall” of “Gh. Asachi” Technical University of Iasi – Faculty of Electronics and Telecommunications, Copou
19:00 – 21:00 **Welcome party** (Technical University Library)
21:00 – 22:00 **IEEE – CPMT Hungary & Romanian Joint Chapter Meeting**

Friday, September 22, 2006

- 07:00 – 08:00 **Breakfast** (“Titu Maiorescu” student restaurant, Copou)
08:00 – 08:30 **Registration** – The “Lost Steps Hall” of “Gh. Asachi” Technical University of Iasi – Faculty of Electronics and Telecommunications, Copou
08:30 – 09:30 **Symposium open ceremony** (Technical University Aula Magna)
Keynote speakers audience.

10:00 – 11:40 **Oral Session I**
Aula Magna (Section S1A)

12:00 – 13:40 **Oral Session II**
Aula Magna (Section S2A)

11:40 – 12:00 **Coffee Break** (Technical University Library)

10:00 – 11:40 **Oral Session II**
Technical University Library
(Section S2B)

12:00 – 13:40 **Oral Session II**
Technical University Library
(Section S2C)

13:40 – 15:00 **Lunch** (“Titu Maiorescu” student restaurant, Copou)

15:00 – 16:40 **Oral Session I**
Aula Magna (Section S1B)

15:00 – 16:40 **Oral Session II**
Technical University Library
(Section S2D)

16:40 – 17:00 **Coffee Break** (Technical University Library)

17:00 – 19:00 – **Poster Session** (The “Lost Steps Hall” of Technical University)

- 19:00 – 20:00 – **Steering Committee Meeting** – Paper Evaluation
20:00 **Banquet** (“Titu Maiorescu” student restaurant, Copou)

Saturday: September 23, 2006

- 07:00 – 08:00 **Breakfast** (“Titu Maiorescu” student restaurant, Copou)
08:00 – 19:00 **Trip**

Trip route: Iasi (departure: 08:00) - Ruginoasa - Pascani – Falticeni (“Ion Irimescu” Sculptor Museum) - Gura Humorului - Voronet Monastery - Vama - Vatra Moldovitei Monastery – “Obcina Mare” Mountain - Sucevita Monastery (lunch at the Monastery) - “Marginea” Black Ceramic Center - Radăuți - Suceava – Iași (dinner; arrival: 19:00).

Sunday: September 24, 2006

- 08:30 – 09:30 **Breakfast** (“Titu Maiorescu” student restaurant, Copou)
09:30 – 10:30 **Closing Session**
SIITME 2006 Awarding
SIITME 2007
Closing Remarks

Towards SIITME 2007 in Baia Mare,

Vlad CEHAN, SIITME 2006 Conference Chairman

Symposium sections

Session I: Electronic packaging - technology, materials, development

Session II: Theory of circuits and computer assisted systems study

Oral section S1A, 10:00 – 11:40, Aula Magna

Chairmen: Paul Svasta, Pavel Mach

Crt. No.	Authors	Title
1	László Gál, Zsolt Illyefalvi-Vitéz, János Pinkola	<i>Selective Surface Finishes of Printed Wiring Boards – Basic Technologies and Comparison of their Features</i>
2	David Bušek, Aleš Duraj, Pavel Mach	<i>Are Electrically Conductive Adhesives Suitable for Every Surface Finish?</i>
3	Ivana Beshajová Pelikánová, Pavel Mach	<i>Changes of Properties of Adhesive Joints caused by temperature</i>
4	Karel Dušek, Jan Urbánek, Pavel Mach	<i>Study of Influence of Working Conditions on Changes of the Surface Tension of Solders</i>
5	Ilie Popa, Ioan Liță	<i>Usage Of Design-For-Test Concept In Designing Of The Electronics Modules</i>
6	Daniela Ionescu, Petruț Duma	<i>HF Study of the Board Materials RoHS Compatible, for Flexible PCBs</i>
7	Ciprian Ionescu, Norocel Codreanu, Virgil Golumbeanu and Paul Svasta	<i>Modeling and Simulation of Integrated Passive Components</i>

Oral section S1B, 15:00 – 16:40, Aula Magna

Chairmen: Zsolt Illyefalvi-Vitéz, Gabriela Matache

Crt. No.	Authors	Title
8	Zsolt Illyefalvi-Vitéz, Oliver Krammer, Janos Pinkola	<i>Soldering and Reliability Trials using Pb-Free Solders</i>
9	Mircea Găvan, Ciprian Ionescu, Dan Constantinescu, Paul Svasta	<i>Study About Thermal and Mechanical Capabilities of Integrated Resistors in Organic Substrates</i>
10	N. Militaru, G. Lojewski, M.G. Banciu	<i>The Influence of the Defected Ground Structures and of the Housing on the Performances of Some Planar Microwave Band-Pass Filters</i>
11	Olivér Krammer and Balázs Illés	<i>Comparative Study of Stencils for Advanced Lead-Free Reflow Soldering Technologies</i>
12	Pavel Mach, David Bušek, Aleš Duraj	<i>Properties of Electrically Conductive Adhesive with Added Ag Nanoparticles</i>
13	S. Jianu, I. Plotog, C. Turcu, A. Stan, T. C. Cucu, N. D. Codreanu	<i>Reliability Aspects for optimal Implementation of Lead-free Technology</i>

Oral section S2A, 12:00 – 13:40, Aula Magna

Chairmen: Alena Pietrikova, George Lojewski

Crt. No.	Authors	Title
14	A. Mirea, A. Marin, G.Matache, I. Dutu	<i>The Theoretical and Experimental Studies about Tuning of the Displacement Units whit the Hydraulic Actuation</i>
15	Alin Grama, Seban Lungu, Ovidiu Pop	<i>Waveform Generator with Walsh Functions</i>
16	Andrei Drumea, Alexandru Vasile, Paul Svasta	<i>Displacement Transducer with LVDT Sensor</i>
17	Antonius Stanciu	<i>A High-Performance Low-Cost Solution for a Computer Driven Rapid Prototyping Tool</i>
18	I. Ciascai, L. Viman, M. Dabacan, S. Pop	<i>Measurement of the Infiltrations Discharges in Hydro-energetic Constructions</i>
19	Cristian Fărcaș, Zoltan Juhos, Niculaie Palaghită and Dorin Petreuş	<i>System in Package for Temperature Control</i>

Oral section S2B, 10:00 – 11:40, Technical University Library
Chairman: János Pinkola, Paul Şchiopu

Crt. No.	Authors	Title
20	Csaba Dominkovics, László Gál, Gábor Harsányi	<i>Qualitative Analysis in the Printed Wiring Board's Manufacturing and the Applied Fractal Theory</i>
21	Cristian Fărcaş, Ionuţ Ciocan, Dorin Petreuş and Niculaie Palaghiţă	<i>Virtual Laboratory of Switch Mode Power Supply</i>
22	D. Bonfert, P. Svasta	<i>ESD Susceptibility of Thin Film Chip Resistors by Means of Transmission Line Pulsing</i>
23	Dorin Petreuş, Zoltan Juhos, Cristian Fărcaş, Niculaie Palaghiţă	<i>Virtual Instrument for Vibration Measurement</i>
24	G. Matache, I. Dutu, I. Ilie	<i>Pressure Regulator with Piezoelectric Converter</i>
25	L. Viman, I. Ciascai, M. Dabacan, S. Pop	<i>Determination the Angular Movements of an Underground Hydroelectric Power Station</i>

Oral section S2C, 12:00 – 13:40, Technical University Library
Chairman: Ioan Liţă, Virgil Golumbeanu

Crt. No.	Authors	Title
26	Marius Muresan, Dan Pitica, Gabriel Chindris	<i>In Circuit Test Tool for FPGA boards using IEEE 1149.1</i>
27	Marius Rangu, Mircea Vladutiu, Paul Svasta	<i>On Rambus Memory System Signal Integrity Issues</i>
28	Mircea Daniel Frunza, Irina Radinschi	<i>A Virtual Model for Testing Mathematics, Physics and Chemistry Student's Knowledge</i>
29	Ovidiu Iancu, Paul Schiopu, Adrian Manea, Ionica Cristea, Monica Nadasan, Neculai Grosu	<i>Autonomous Remote Sensing with SAW Devices</i>
30	Ovidiu Pop	<i>A New DC Brushless Motor Driver Based on Motorola Microcontroller</i>

Oral section S2D, 15:00 – 16:40, Technical University Library
Chairman: Dan Pitica

Crt. No.	Authors	Title
31	Paul Svasta, Mihai Vasiliu, Ciprian Ionescu, Alexandru Romanescu, Alena Pietrikova	<i>Minimising Planar Inductor Losses under Non-sinusoidal Waveforms</i>
32	Radu Gabriel Bozomitu, Vlad Cehan	<i>A New Fully Balanced Differential OTA with Common-Mode Feedback Detector</i>
33	Reinhold Frosch, Dan Tudor Vuza	<i>Improving the Reliability of Detection of RFID Tags by Synchronous Demodulation with Programmable Phase Shift</i>
34	Septimiu Pop, Ioan Ciascai, Liviu Viman, Mircea Dabacan	<i>Error Detection and Substraction from Resistive Sensor Measurements</i>
35	Sorin Puscoi, Florin Serbanescu	<i>Telemonitoring Systems for Medical Applications</i>

**Poster session, 17:00 – 19, The “Lost Steps Hall” of Technical University
Evaluation commission: Zsolt Illyefalvi-Vitéz, Dan Pitica, Petruț Duma**

Crt. No.	Authors	Session	Title
1	A. Stan, S. Jianu, I. Plotog, C. Turcu, T. C. Cucu, N. D. Codreanu	I	<i>IPC Certifications & Lead-Free Programs, an Industry-Developed Courses for Companies Look for ISO Certifications</i>
2	Csaba-Zoltán Kertész	I	<i>Designing Single Sided PCB with Crossover Layers</i>
3	Daniel Simion-Zanescu, Paul Svasta	I	<i>Enhanced Model for Reflow Soldering Process</i>
4	I. Plotog, S. Jianu, C. Turcu, A. Stan, T. C. Cucu, N. D. Codreanu	I	<i>DFM Concept for Wave Soldering Technology</i>
5	Marius Rangu	I	<i>Using Area Fills to Improve the Manufacturability of Printed Circuit Boards</i>
6	Alexandru Vasile, Liviu Nastase, Paul Svasta	II	<i>Over Voltage Protection Methods In Automotive Electronic Circuits</i>
7	Antoni Stanciu, Doina Drăgulescu, Loredana Ungureanu	II	<i>A Hydraulic Solution for Implementing Human’s Hand Prehension Function</i>
8	Brindusa Ciobanu, Irina Radinschi, Alexandru Tesoi	II	<i>A Computational Algorithm Adapted for the Experimental Study of the Electrical Properties of Samples</i>
9	Daniel Alexandru Visan, Ioan Lita	II	<i>GPS Receiver with LabVIEW Interface</i>
10	Daniela Ionescu, Radu Gabriel Bozomitu	II	<i>Electrical Properties Analysis for a Liquid Crystal Class of Materials</i>
11	Paul Svasta, Hapenciuc Iaroslav-Andrei	II	<i>Data Acquisition System for Process Control In Dangerous Environments</i>
12	Ioan Lita, Daniel Alexandru Visan	II	<i>Remote Data Transmission for Distributed Acquisition Systems</i>
13	Ioan Lita, Rodica Mihaela Teodorescu, Ion Bogdan Cioc	II	<i>Aspects Regarding Analysis And Design Of Cebâsev Low Pass Filters</i>
14	Ionut Schiopu	II	<i>Fast Reading of MRAM Using an Adaptable Circuit</i>
15	Irina Bristena Vasile, Sorin Puscoici, Alexandru Vasile	II	<i>Evaluation of Voice Signal Quality in VoDSL Networks</i>
16	Brindusa Ciobanu, Irina Radinschi	II	<i>Implementation of Physics Teaching in Engineering Education</i>
17	Mira Caldararu, Alexandru Vasile, Florin Caldararu	II	<i>Analytical Procedure For Multi Gas Detection From SnO₂ Semiconductor Sensor</i>
18	Paul Svasta, Hapenciuc Iaroslav-Andrei	II	<i>Inertial Navigation Module</i>
19	Paul Svasta, Hapenciuc Iaroslav-Andrei	II	<i>Ultrasonic Anemometer with Electronic Compass</i>
20	Petre Ogruțan, Carmen Gerigan, Lia Aciu	II	<i>Fujitsu Microcontroller-Based Embedded Control System for Asynchronous Motors</i>
21	Petruț Duma, Daniela Ionescu	II	<i>Logic Analyzer for Application Systems Equipped with AT89C51 Family Microcontroller</i>
22	Radu Gabriel Bozomitu, Daniela Ionescu	II	<i>A New Pseudo-Differential Operational Transconductance Amplifier</i>
23	Septimiu Pop, Ioan Ciascai, Mircea Dabacan, Liviu Viman	II	<i>Seismic Trigger Using MEMS Sensor</i>
24	Ștefan Oprea, Ioan Liță, Dan Alexandru Vișan	II	<i>Image Subtraction Applications</i>
25	Ștefan Oprea, Ion Bogdan Cioc	II	<i>Image Segmentation Used For Surveillance of Robots Arms</i>